



Virtual University

About Us

CS401
Solved Final Term Paper 5

Waqar.siddhu@gmail.com

Year
2017

For More Plz Visit

WWW.VirtualAcademyLive.com

بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

In the Name of Allāh, the Most Gracious, the Most Merciful

Paper Pattern

MCQS 40 each 1 mark
Short 4 each 2 marks
Short 4 each 3 marks
long 4 each 5 marks

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

- ☐ AX register will reside on the stack
- ☒ A copy of AX will go on the stack
- ☐ The value of AX disappear after moving on stack
- ☐ Stack will send an acceptance message

Pg 69

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

- ☒ IRQ 4
- ☐ IRQ 5
- ☐ IRQ 6
- ☐ IRQ 7

pg 114

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ Handler

pg 114

☐ Processor

☐ IRQ

☐ PIC

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ 1.193MHZ

pg 122

☐ 2.193MHZ

☐ 3.193MHZ

☐ 4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ Key board interrupt

☒ Timer interrupt

pg 122

☐ INT 2

☐ INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ 1AB00

☐ 001AB

☒ BB00F

☐ F00BB

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ 01010000

☐ 10001010

☐ 00010100

☐ 01010111

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ An IRQ Collision

☒ An IRQ Conflict

pg 114

☐ An IRQ Crash

☐ An IRQ Blockage

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ Decrement by 1

pg 92

☐ Decrement by 2

☐ Increment by 1

☐ Increment by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ In general the memory cell cannot be wider than data bus

☐ Control bus is unidirectional

☐ Memory can use the address bus to refer to certain register in processor

~~not confirm~~

☐ There is no relation between memory and data bus

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ Code Segment register

☐ Data Segment register

☒ Accumulator register

pg 12

☐ Stack segment register

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐

Single word can not be pushed on the stack

☐

Single word can not be popped from the stack

☒

Single byte can not be popped from the stack

pg 68

☐

Single quad can be popped from the stack

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐

The dimensions of the screen

☐

Character position on the screen

☐

Character color on the screen

☒

ASCII code of the character

pg 81

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒

BIOS video

pg 149

☐

DOS video

☐

RAM

☐

Disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

line

☐

dot

☒

pg 149

circle

☐

rectangle

☐

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

The value of AH register in the write Graphics pixel service is _____.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

0Ch

☒

pg 152

0Bh

☐

1Ch

☐

2Ch

☐

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

control block

☐



pg 140

Process control block

☒

stack

☐

hard disk

☐

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ a) al

pg 152

☐ b) bl

☐ c) cl

☐ d) bh

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ a) Ultra resolution mode

☒ b) High resolution mode

pg 180

☐ c) Medium resolution mode

☐ d) Low resolution mode

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ a) Variable Electronics Standards Association

☒ b) Video Electronics Standards Association

☐ c) Video Energy Standards Association

☐ d) Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ Video BIOS Extensions

☐ Video BIOS Emergence

☐ Video Binary Extensions

☐ Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ 16 bit

☐ 20 bit

☐ 32 bit

☒ 48 bit

pg 182

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ Programming mode

☒ Real time mode

not confirm

☐ Visualization mode

☐ Memory mode

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

First Byte

☐

Second Byte

☒

pg 152

Third Byte

☐

Last Byte

☐

Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

128 Bytes

☐

256 Bytes

☐

512 Bytes

☒

pg 155

1024 Bytes

☐

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

INT 10

☐

INT 09

☐

INT 08

☒

pg 141

INT 07

☐

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ global

☒ local

pg 141

☐ legal

☐ illegal

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

Technically LAPX88 stands for " _____ Advanced Processor Extensions 88."

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ Intel

pg 14

☐ Integrated

☐ Intra

☐ Internal

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In Left-Shift-Operation, the left most bit _____

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ will be dropped

☒ will go into CF

☐ will be moved to the right-most position

☐ will always be 1

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ 9 pin DB-9

pg 171

☐ 8 pin DB-9

☐ 3 pin DB-9

☐ 9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ 6

☐ 7

☒ 8

pg 171

☐ 9

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ 1

pg 171

☐ 2

☐ 3

☐ 4

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ INT14

pg 171

☐ INT11

☐ INT12

☐ INT13

Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ 16 bit processor

pg 14

☐ 32 bit processor

☐ 64 bit processor

☐ 128 bit processor

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ 2GB

☒ 4GB

☐ 6GB

☐ 8GB

Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ 1

☒ 0

pg 175

☐ -1

☐ -2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a _____ program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐ 8 bit

☐ 16 bit

☒ 32 bit

pg 191

☐ 64 bit

Made by: Waqar Siddhu

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a 32 bit _____ that holds the address of currently executing instruction.

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☒ Program counter

pg 191

☐ Stack pointer

☐ Register

☐ Stack

Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐

One input signals and eight output signals

☐

One input signal and one output signal

☒

Eight input signals and one output signals

pg 113

☐

Eight input signals and eight output signals

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

WWW.VirtualAcademyLive.com

☐

INT 0

☒

INT 1

pg 105

☐

INT 2

☐

INT 3

Made by: Waqar Siddhu

Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer (Please [click here](#) to Add Answer)

WWW.VirtualAcademyLive.com

Normal Arial 12 B I U

Made by: Waqar Siddhu